

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on January 7, 2004.

Stephanie Daryae  
Stephanie Daryae

Applicant : Janardhanan S. Ajit  
Application No. : 10/621,005  
Filed : July 16, 2003  
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT  
  
Grp./Div. : 2816  
Examiner : Tuan Thieu Lam  
  
Docket No. : 50716/DBP/B600

## TRANSMITTAL OF DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
January 7, 2004

Commissioner:

Enclosed is 1 sheet of drawing for this application. As discussed with the Examiner, it is being submitted as corrected drawing to replace the corresponding formal drawing as originally filed. Also enclosed is an "Annotated Marked-up Drawing" showing the changes made.

Respectfully submitted,

CHRISTIE, PARKER &amp; HALE, LLP

By Jun E. Jeon  
Jun-Young E. Jeon  
Reg. No. 43,693  
626/795-9900

JEJ/sd

SD PASS44065.1-01/7/04 10:11 AM